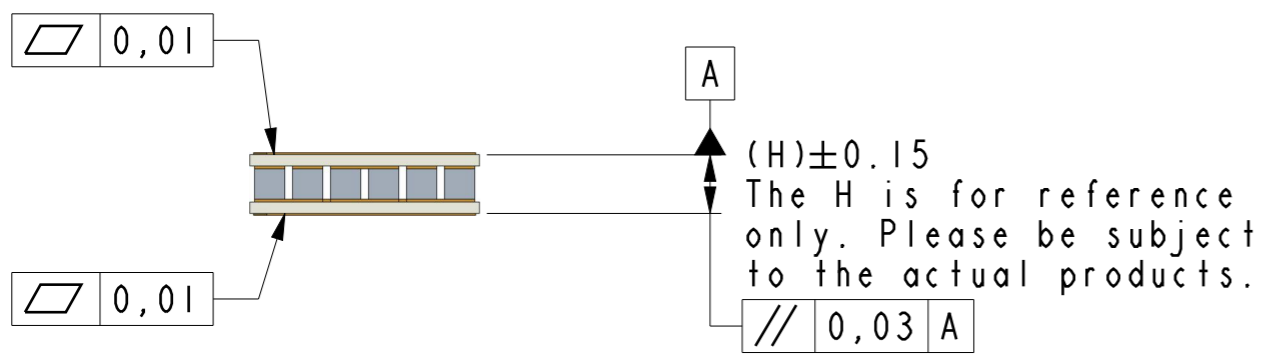
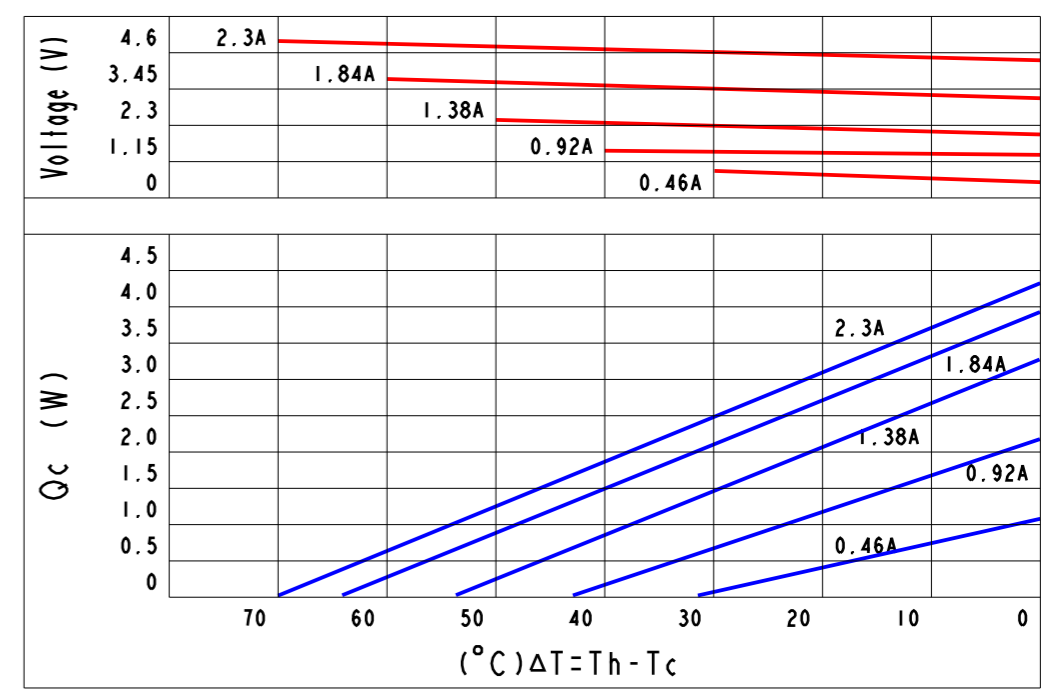


REVISIONS						
REV.	POS.	DESCRIPTION	DATE	DRW	APP	ECR#
A		INITIAL CREATION	2013/09/09	Gary	Mason	



Curve Chart(Be Confined To TESI-035236112):



Part Number And Feature:

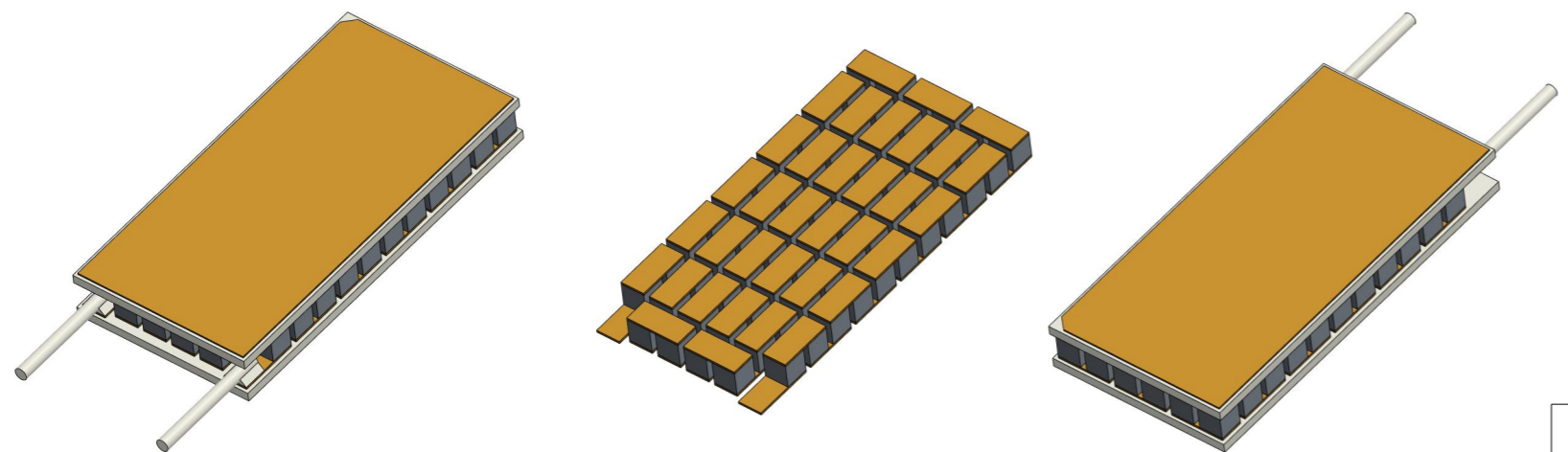
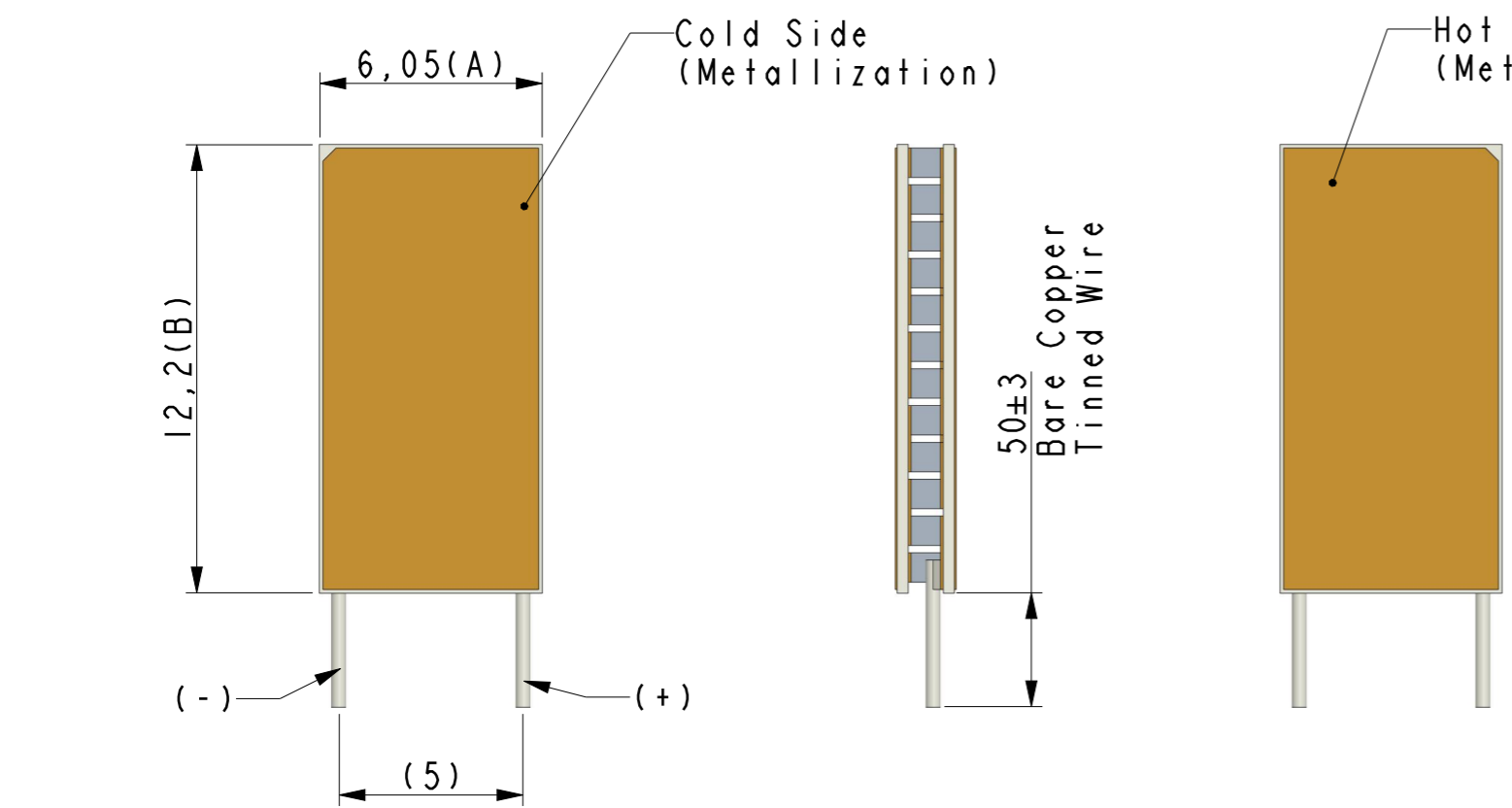
T	E	S	I	-	0	3	5	x	x	6	1	1	2	Sealing	NO
↓	↓				↓	↓	↓	↓	↓	↓	↓	↓	↓	Operation Temperature	200 $^{\circ}$ C(Max.)
Thermo	Electric	Chip(Small)	Stage	Stack	N & P	Stack	Quantity	Current	A(Max.)	Dimension	(A)	Dimension	(B)	Melting Point	235 $^{\circ}$ C
														Storage Temperature	-60 $^{\circ}$ C~100 $^{\circ}$ C
														RoHS	YES

Technical Data:

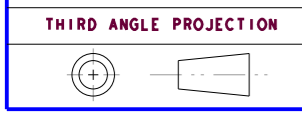
ITEM	Part Number	P&N Couple	A(Max.)	V(Max.)	$Q_c$ (W) / $T_h=27^{\circ}\text{C}$ / $\Delta T(^{\circ}\text{C})$	DIM(A)	DIM(B)	DIM(H)
1	TESI-035106112	35	1.0 A	4.6 V	2.3W	81 $^{\circ}$ C	6.05	12.2 RFI.65
2	TESI-035236112	35	2.3 A	4.6 V	4.5W	81 $^{\circ}$ C	6.05	12.2 RFI.65

Notes:

1. Printing always on cold side.
2. Torlerance of thermo and electric parameters  $\pm 10\%$ .
3. Please mount heat sink before you use it. also, please do not exceed the extra voltage at any time.



\*DO NOT SCALE DRAWING



THIS DRAWING AND THE DATA DISCLOSED HEREIN OR HEREWITH IS NOT TO BE REPR ODUCE  
USED OR DISCLOSED OR IN PART TO ANYONE WITHOUT THE PERMISSION OF KJLP (SHENZHEN) ELECTRONICS  
CO., LTD.

1. UNLESS OTHERWISE SPECIFIED,  
DIMENSIONS ARE MM  
2 TOLERANCE ARE AS FOLLOWS:  
0 < X < 2 ± 0.06  
2 < X < 10 ± 0.08  
10 < X < 50 ± 0.12  
50 < X < 100 ± 0.16  
100 < X < 200 ± 0.20  
200 < X < 300 ± 0.30  
ANGLES ± 0.5 $^{\circ}$

PART No.	TESI-035xx6112	DESCRIPTION	DC 4.6V(Max.), 1.0~2.3A(Max.), 35 P&N, 6.05*12.2mm		
SIGNATURE		DATE	昆晶冷片(深圳)电子有限公司		
DRAWN BY	Gary	2013/09/09	KJLP (SHENZHEN)ELECTRONICS CO., LTD		
CHECKED BY	Justin	2013/09/09	email: kjlp@kjlp.net http:// www.kjlp.net		
ENGR	Vivi	2013/09/09	Tel: +86-755-82528352 Fax: +86-755-22639899		
APPROVED BY	Mason	2013/09/09	CAD MODLE:	TESI-035xx6112.prt	SCALE: 1:1 REV: A
ISSUED BY	Jack	2013/09/09	CAD DWG:	TESI-035xx6112.drw	SIZE: A3 SHEET: 1 OF 1