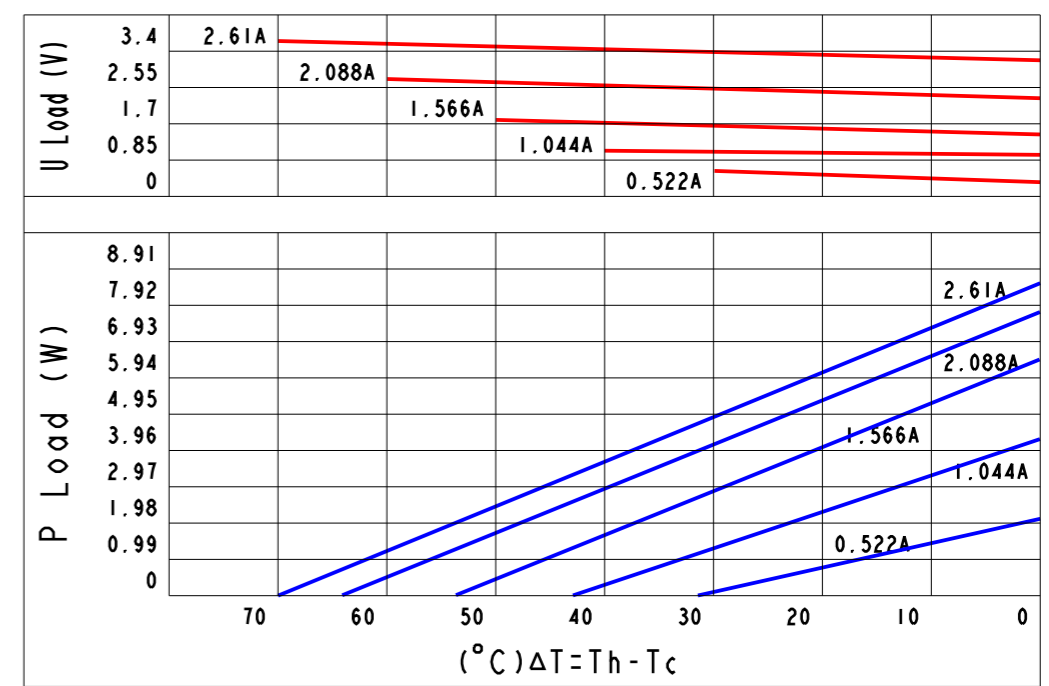


REVISIONS					
REV.	POS.	DESCRIPTION	DATE	DRW	APP
A		INITIAL CREATION	2013/09/09	Gary	Mason

Curve Chart(Be Confined To TEG1-127135050):



Part Number And Feature:

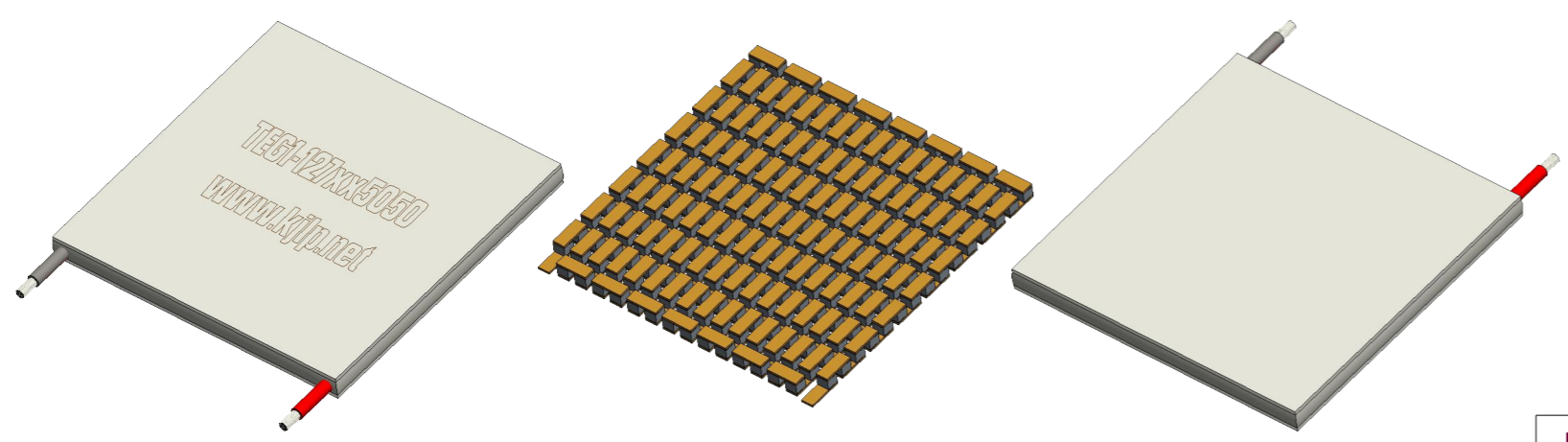
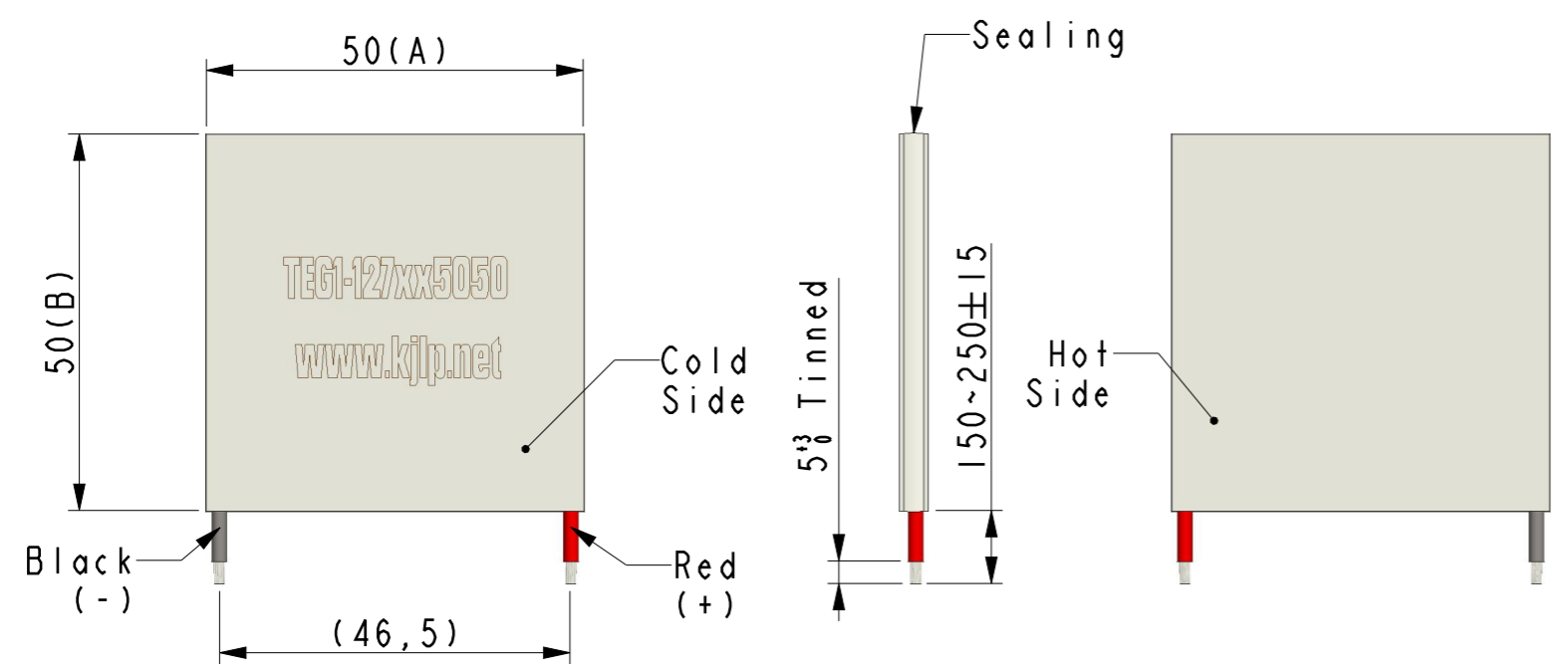
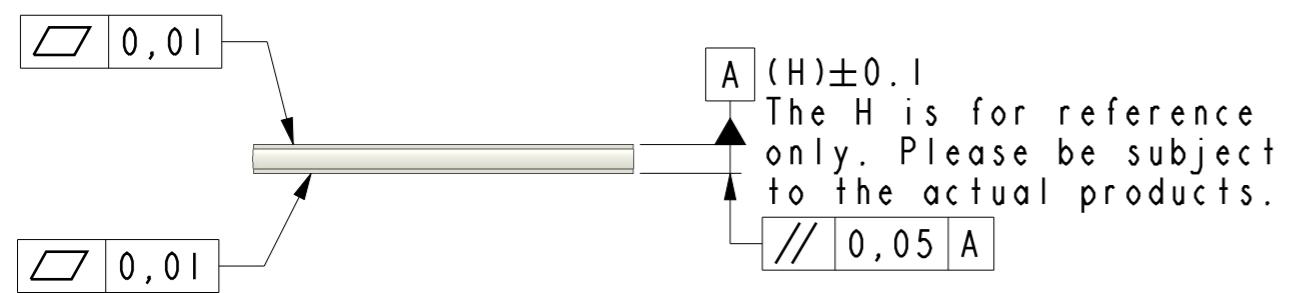
T	E	G	I	-	1	2	x	x	5	0	5	0	Sealing	YES	
↓	↓				↓	↓	↓	↓	↓	↓	↓	↓	Operation Temperatur(Max.)	200°C(Max.)	
Thermo	Electric	Chip	Stage	Stack	N & P	Stack	Quantity	R	Load	Dimension	(A)	Dimension	(B)	Melting Point	235°C
														Storage Temperature	-60°C~100°C
														RoHS	YES

Technical Data:

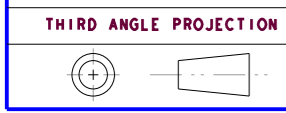
ITEM	Part NO.	Stack(P&N)	U Load(Max.)	P Load(Max.)	R Load(Max.)	Uoc	P/N Size	DIM(A)	DIM(B)	DIM(H)
1	TEG1-127165050	127	3.4 V	7.4 W	1.6 Ohm	6.4 V	1.6*2.0*2.0	50	50	RF3.8
2	TEG1-127135050	127	3.4 V	8.9 W	1.3 Ohm	6.4 V	1.3*2.0*2.0	50	50	RF3.6

Notes:


1. Printing always on cold side.
2. Tolerance of thermo and electric parameters  $\pm 10\%$ .
3. Please mount heat sink before you use it as cooler. also, please do not exceed the extra voltage at any time.



\*DO NOT SCALE DRAWING



THIS DRAWING AND THE DATA DISCLOSED HEREIN OR HEREWITH IS NOT TO BE REPR ODUCE  
USED OR DISCLOSED OR IN PART TO ANYONE WITHOUT THE PERMISSION OF KJLP (SHENZHEN) ELECTRONICS  
CO., LTD.

<p>1. UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE MM 2 TOLERANCE ARE AS FOLLOWS: 0 &lt; X &lt; 2 ± 0.06 2 &lt; X &lt; 10 ± 0.08 10 &lt; X &lt; 50 ± 0.12 50 &lt; X &lt; 100 ± 0.16 100 &lt; X &lt; 200 ± 0.20 200 &lt; X &lt; 300 ± 0.30 ANGLES ± 0.5°</p>	PART No.	TEG1-127xx5050	DESCRIPTION	3.4V(Max.), 7.4~8.9W(Max.), 127 P&N, 50*50mm			
	SIGNATURE		DATE	 <p>昆晶冷片(深圳)电子有限公司 KJLP (SHENZHEN) ELECTRONICS CO., LTD email: kjlp@kjlp.net http:// www.kjlp.net Tel: +86-755-82528352 Fax: +86-755-22639899</p>			
	DRAWN BY	Gary	2013/09/09				
	CHECKED BY	Justin	2013/09/09				
	ENGR	Vivi	2013/09/09				
APPROVED BY	Mason	2013/09/09					
MATERIAL:	ISSUED BY	Jack	2013/09/09	CAD MODLE:	TEG1-127xx5050.prt	SCALE: 1:1	REV: A
				CAD DWG:	TEG1-127xx5050.drw	SIZE: A3	SHEET: 1 OF 1