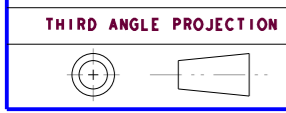


- Notes:
1. Printing always on cold side.
 2. Torlerance of thermo and electric parameters $\pm 10\%$.
 3. Please mount heat sink before you use it. also, please do not exceed the extra voltage at any time.
 4. Please contact with us if you need Melting Point 183°C (Operation Temperature 150°C Max.) and 235°C (Operation Temperature 200°C Max.) type.

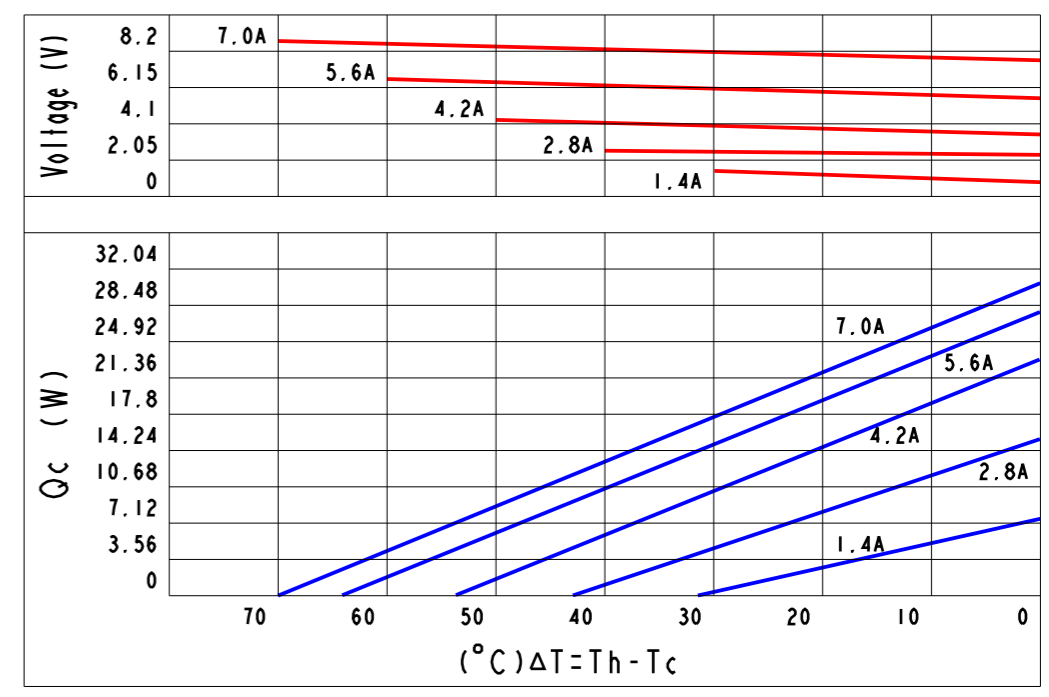
*DO NOT SCALE DRAWING



THIS DRAWING AND THE DATA DISCLOSED HEREIN OR HEREWITH IS NOT TO BE REPR ODUCE
USED OR DISCLOSED OR IN PART TO ANYONE WITHOUT THE PERMISSION OF KJLP (SHENZHEN) ELECTRONICS
CO., LTD.

REVISIONS					
REV.	POS.	DESCRIPTION	DATE	DRW	APP
A		INITIAL CREATION	2013/09/09	Gory	Mason

Curve Chart(Be Confined To TECI-069072030):



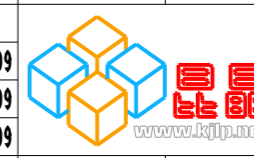
Part Number And Feature:

T	E	C	I	-	0	6	9	x	x	2	0	3	0	Sealing	YES
↓	↓				↓	↓	↓	↓	↓	↓	↓	↓	↓	Operation Temperature	125°C (Max.)
Thermo	Electric	Chip	Stage	Stack	N & P	Stack	Quantity	Current	A(Max.)	Dimension	(A)	Dimension	(B)	Melting Point	138°C
														Storage Temperature	$-60^{\circ}\text{C} \sim 100^{\circ}\text{C}$
														RoHS	YES

Technical Data:

ITEM	Part NO.	Stack(P&N)	A(Max.)	V(Max.)	Qc(W) /Th=27°C/ ΔT(°C)	DIM(A)	DIM(B)	DIM(H)
1	TECI-069032030	69	2.5 A	8.2 V	13.2W	70°C	20	30
2	TECI-069072030	69	7 A	8.2 V	32W	70°C	20	30

1. UNLESS OTHERWISE SPECIFIED,
DIMENSIONS ARE MM
2 TOLERANCE ARE AS FOLLOWS:
0 < X < 2 ± 0.06
2 < X < 10 ± 0.08
10 < X < 50 ± 0.12
50 < X < 100 ± 0.16
100 < X < 200 ± 0.20
200 < X < 300 ± 0.30
ANGLES ± 0.5°

PART No.	TECI-069xx2030	DESCRIPTION	DC 8.2V(Max.), 2.5~7A(Max.), 69 P&N, 20*30mm			
SIGNATURE		DATE	 昆晶冷片(深圳)电子有限公司 KJLP (SHENZHEN) ELECTRONICS CO., LTD email: kjlp@kjlp.net http:// www.kjlp.net Tel: +86-755-82528352 Fax: +86-755-22639899			
DRAWN BY	Gory	2013/09/09				
CHECKED BY	Justin	2013/09/09				
ENGR	Vivi	2013/09/09				
APPROVED BY	Mason	2013/09/09				
ISSUED BY	Jack	2013/09/09	CAD MODLE:	TECI-069xx2030.prt	SCALE: 1:1	REV: A
MATERIAL:		CAD DWG:	TECI-069xx2030.drw	SIZE: A3	SHEET: 1 OF 1	