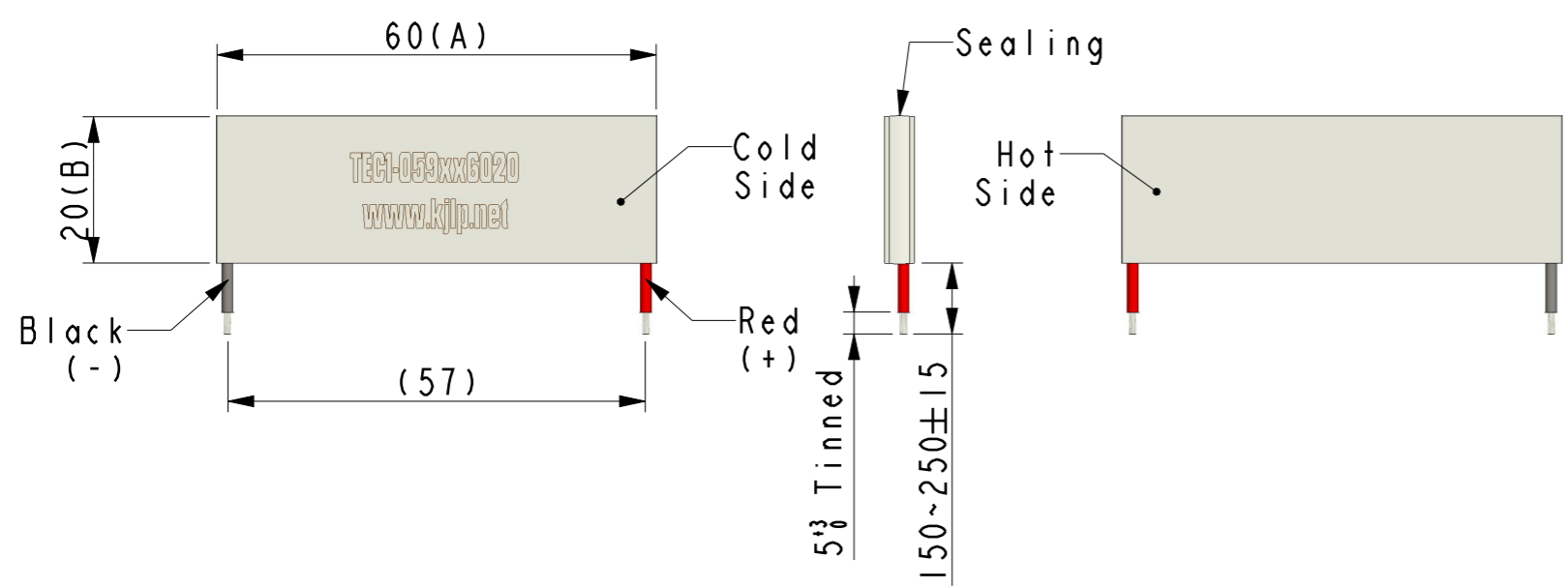
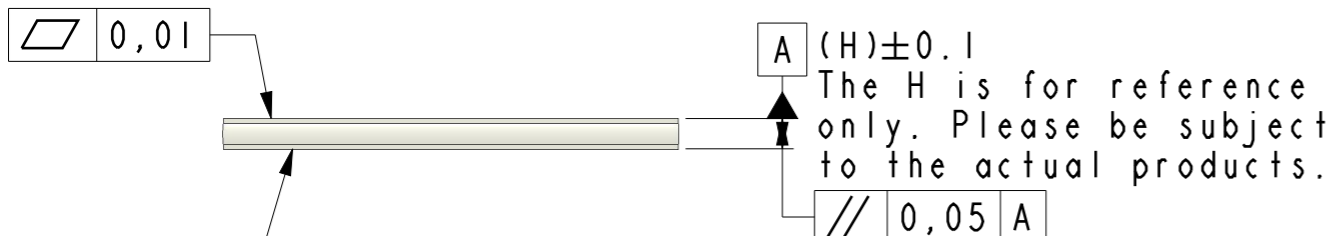
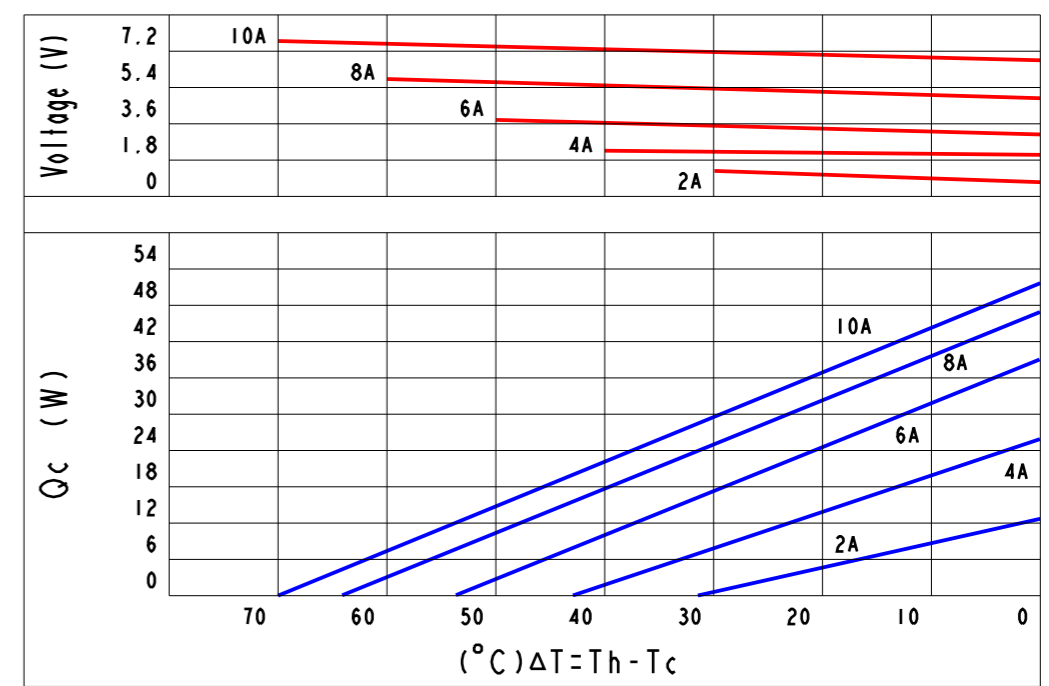


REVISIONS					
REV.	POS.	DESCRIPTION	DATE	DRW	APP
A		INITIAL CREATION	2013/09/09	Gary	Mason



Curve Chart(Be Confined To TECI-059106020):



Part Number And Feature:

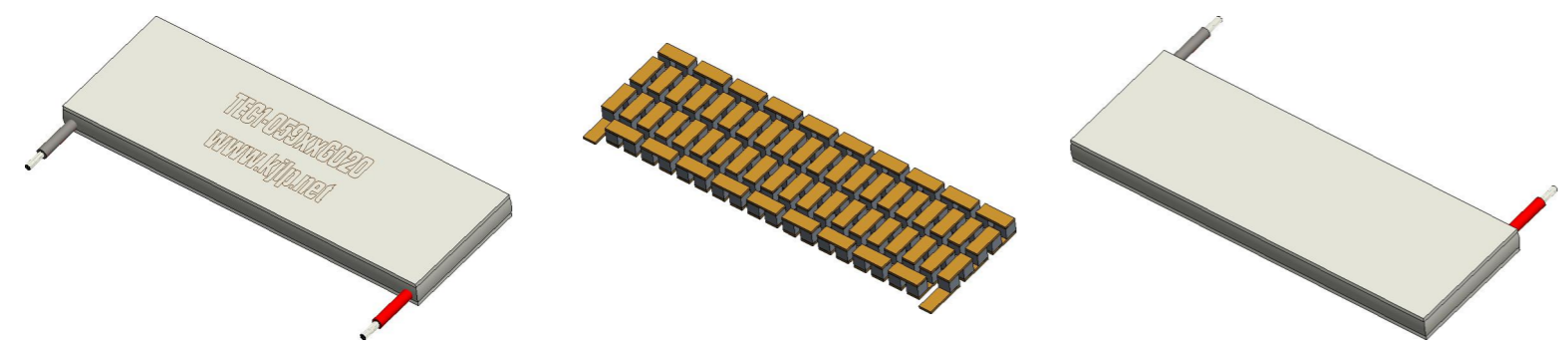
T	E	C	I	-	0	5	9	x	x	6	0	2	0	Sealing	YES
↓	↓				↓	↓	↓	↓	↓	↓	↓	↓	↓	Operation Temperature	125°C(Max.)
Thermo	Electric	Chip	Stage	Stack	N & P	Stack	Quantity	Current	A(Max.)	Dimension	(A)	Dimension	(B)	Melting Point	138°C
														Storage Temperature	-60°C~100°C
														RoHS	YES

Technical Data:

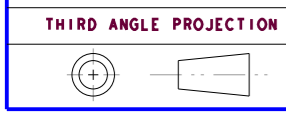
ITEM	Part NO.	Stack(P&N)	A(Max.)	V(Max.)	Qc(W) /Th=27°C/ ΔT(°C)	DIM(A)	DIM(B)	DIM(H)
1	TECI-059106020	59	10 A	6.9 V	45W / 70°C	60	20	RF4.0

Notes:

1. Printing always on cold side.
2. Tolerance of thermo and electric parameters ±10%.
3. Please mount heat sink before you use it, also, please do not exceed the extra voltage at any time.
4. Please contact with us if you need Melting Point 183°C (Operation Temperature 150°C Max.) and 235°C (Operation Temperature 200°C Max.) type.




*DO NOT SCALE DRAWING



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USED OR DISCLOSED OR IN PART TO ANYONE WITHOUT THE PERMISSION OF KJLP (SHENZHEN) ELECTRONICS
CO., LTD.

1. UNLESS OTHERWISE SPECIFIED,
DIMENSIONS ARE MM
2 TOLERANCE ARE AS FOLLOWS:
0 < X < 2 ± 0.06
2 < X < 10 ± 0.08
10 < X < 50 ± 0.12
50 < X < 100 ± 0.16
100 < X < 200 ± 0.20
200 < X < 300 ± 0.30
ANGLES ± 0.5°

PART No.	TECI-059xx6020	DESCRIPTION	DC 6.9V(Max.), 10A(Max.), 59 P&N, 60*20mm						
SIGNATURE		DATE	 昆晶冷片(深圳)电子有限公司 KJLP (SHENZHEN) ELECTRONICS CO., LTD email: kjlp@kjlp.net http:// www.kjlp.net Tel: +86-755-82528352 Fax: +86-755-22639899						
DRAWN BY	Gary	2013/09/09							
CHECKED BY	Justin	2013/09/09							
ENGR	Vivi	2013/09/09							
APPROVED BY	Mason	2013/09/09							
MATERIAL:	ISSUED BY	Jack	2013/09/09	CAD DWG:	TECI-059xx6020.drw	SCALE:	1:1	REV:	A
				CAD MODLE:	TECI-059xx6020.prt	SIZE:	A3	SHEET:	1 OF 1